

Alg. sum of all volt. & volt. loop in a closed circuit / path is eq. to zero : KVL

Alg. sum of all currents meeting at a junction or a node is eq. to zero : KCL.

x. The time taken by an alternating quantity to complete one cycle:

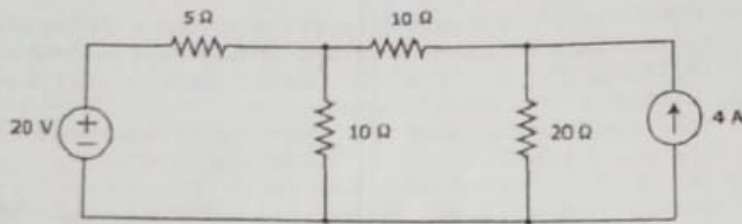
- (A) Time period (B) Frequency (C) Angular Velocity (D) Time constant

Q.2 i. State & Explain Kirchoff's Law.

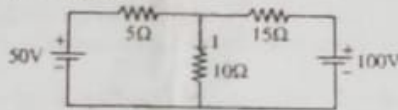
ii. Define the following:

- i. Active Elements
- ii. Passive Elements
- iii. Energy sources

iii. By using Nodal analysis, find the current in all branches of the network shown in the figure.



OR iv. For the circuit shown in figure determine the current I through the 10Ω resistance by Thevenin's Theorem.



Q.3 i. Explain and derive the expression for Average value and RMS value for sinusoidal current

ii. A coil of resistance 10Ω & inductance $0.1H$ is connected in series with $150\mu F$ capacitor across a $200V, 50Hz$ supply. Calculate—

- (i) Inductive Reactance
- (ii) Capacitive Reactance
- (iii) Impedance
- (iv) Current
- (v) Power factor
- (vi) Voltage across the coil

OR iii. Define the following:

- (A) Cycle
 - (B) Time period - Time taken by an alternating quantity to complete 1 cycle
 - (C) Frequency - no. of cycles per second
 - (D) Amplitude - peak max. value of an alternating quantity
 - (E) Lagging current by angle ϕ
 - (F) Leading current by angle ϕ
- $F = 1/T$

1 complete cycle
freq. - no. of
half of
any
alternating
quantity

no. of cycles per second
Hertz
[$F = 1/T$]

Total No. of Questions: 4

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Enrollment No.....



Faculty of Engineering
Mid Sem I Examination April -2022
EN3ES17 Basic Electrical Engineering

Programme: B.Tech.

Duration: 2 Hrs.

Branch/Specialisation: All

Maximum Marks: 40

- Q.1
- i. When two resistors are connected in series total resistance is 16Ω and when connected in parallel, equivalent resistance is 4Ω . Values of resistances are:
(A) 11Ω and 5Ω (B) 12Ω and 4Ω (C) 8Ω and 8Ω (D) 15Ω and 1Ω 1
 - ii. Kirchhoff's laws are useful in determining—
(A) Current flowing in a circuit (B) EMFs and Voltage drops in a circuit
(C) Power in a circuit (D) All the above 1
 - iii. In a DC Circuit, Inductive reactance would be—
(A) Equal As in AC Circuits (B) High (C) Extremely High (D) Zero 1
 - iv. In order to determine the thevenin's voltage across the load, the load terminal gets—
(A) Open Circuited (B) Short circuited
(C) Either (a) or (b) (D) Neither (a) nor (b) 1
 - v. According to Thevenin's theorem, any bilateral network can be replaced by a network with—
(A) An independent current source in parallel to the equivalent resistance
(B) An independent voltage source in series with the equivalent resistance
(C) An independent voltage source in parallel to the resistance
(D) None of these 1
 - vi. In sinusoidal wave, RMS value = ___ x Maximum value
(A) 0.636 (B) 0.85 (C) 0.607 (D) 0.707 1
 - vi. The voltage and current relationship for inductor of inductance L is given as—
(A) $v(t) = i(t) * L$ (B) $v(t) = \frac{1}{L} \frac{di(t)}{dt}$
(C) $v(t) = L \frac{di(t)}{dt}$ (D) None of the above 1
 - vi. Two sinusoidal quantities are said to be phase quadrature, when their phase difference is
(A) 0° (B) 30° (C) 45° (D) 90° . 1
 - ix. A $100 \mu\text{F}$ capacitor supplied from 3 V source with a frequency of 50 Hz. The capacitive reactance is
(A) 63.68Ω (B) 15.92Ω (C) 31.84Ω (D) 7.96Ω . 1

$$X_c = \frac{1}{2\pi f C} = \frac{1}{2 \times 3.14 \times 50 \times 100 \times 10^{-6}}$$